

## TITLE OF THE INVENTION

## IMAGE READING APPARATUS AND ITS CONTROL METHOD

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## FIELD OF THE INVENTION

The present invention relates to an image reading apparatus which can transfer a read image signal to an external apparatus via a communication unit, and its control method.

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## BACKGROUND OF THE INVENTION

As a conventional image reading apparatus which reads a document image, converts it into an electrical analog image signal, converts the analog image signal into a digital signal, and transfers the digital signal to an external apparatus, an apparatus shown in, e.g., Fig. 8 is known.

Fig. 8 is a block diagram showing an example of the arrangement of a conventional image reading apparatus.

This image reading apparatus 100 turns on a light source 115 mounted on an optical unit upon receiving a read command from a host computer 121, and forms an image of light reflected by a document on an image sensor 130, thus reading an image for one line in the main scan direction. Meanwhile, the optical unit moves in the sub-scan direction by the drive force of a pulse

motor 117. That is, the image reading apparatus 100 repeats to read a line image in the main scan direction while moving the optical unit in the sub-scan direction.

A system controller & image processor 129  
5 executes various kinds of control in accordance with commands from the host computer 121, and executes various image processes for an image signal converted into a digital signal. The system controller & image processor 129 comprises a CPU 129a, which monitors  
10 communications to detect disconnection/connection of a USB cable 122 as a communication unit with the host computer 121.

However, the conventional image reading apparatus must inevitably comprise the CPU 129a to detect  
15 disconnection/connection of the communication unit 122 and to detect any abnormality. For this reason, an image reading apparatus with a simple structure which does not incorporate any CPU and operates in response to each control command sent from the host computer 121  
20 via the communication unit 122 cannot detect disconnection/connection of the communication unit 122.

Also, in such image reading apparatus with a simple structure which does not incorporate any CPU, when the communication unit 122 is disconnected, since  
25 no control command from the host computer 121 can be received, the subsequent operation becomes unstable,

and the unit continues to be energized, resulting in an increase in consumption power.

#### SUMMARY OF THE INVENTION

5       The present invention has been made in consideration of the above situation, and has as its object to allow even an image reading apparatus with a simple arrangement, which does not incorporate any CPU or the like, to detect communication disconnection, and  
10   to attain power savings in the apparatus.

      According to the present invention, the foregoing object is attained by providing an image reading apparatus which comprises an image sensing unit for reading an image, and a communication unit for  
15   transferring an image signal read by the image sensing unit to an external apparatus, comprising: a detector for detecting presence/absence of abnormality of the communication unit; and a controller for, when the detector detects any abnormality of the communication  
20   unit, setting the image reading apparatus in a power saving mode.

      Further, according to the present invention, the foregoing object is also attained by providing a control method for an image reading apparatus which  
25   comprises an image sensing unit for reading an image, and a communication unit for transferring an image signal read by the image sensing unit to an external

apparatus, comprising: a detection step of detecting presence/absence of abnormality of the communication unit; and a control step of setting, when any abnormality of the communication unit is detected in  
5 the detection step, the image reading apparatus in a power saving mode.

Furthermore, an according to the present invention, the foregoing object is also attained by providing an image processing system which comprises an  
10 image reading apparatus for outputting an image signal read by an image sensing unit to a communication unit, and a host apparatus for processing the image signal sent from the image reading apparatus via the communication unit, the image reading apparatus  
15 comprising: a detector for detecting presence/absence of abnormality of the communication unit; and a controller for, when the detector detects any abnormality of the communication unit, setting the image reading apparatus in a power saving mode.

20 It is another object of the present invention to allow even an image reading apparatus which uses a CPU or the like to detect communication disconnection by a simple arrangement, and to attain power savings in the apparatus.

25 According to the present invention, the foregoing object is attained by providing a storage medium that stores a program for implementing a control method for

an image reading apparatus which comprises an image sensing unit for reading an image, a communication unit for transferring an image signal read by the image sensing unit to an external apparatus, and a detector  
5 for detecting presence/absence of abnormality of the communication unit, comprising: computer readable program code means for, when the detector detects any abnormality of the communication unit, setting the image reading apparatus in a power saving mode.

10 Other features and advantages of the present invention will be apparent from the following description taken in conjunction with the accompanying drawings, in which like reference characters designate the same or similar parts throughout the figures  
15 thereof.

#### BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are incorporated in and constitute a part of the specification,  
20 illustrate embodiments of the invention and, together with the description, serve to explain the principles of the invention.

Fig. 1 is a block diagram showing the arrangement of an image reading apparatus according to a first  
25 embodiment of the present invention;

Fig. 2 is a block diagram showing the arrangement of principal part of the image reading apparatus of the first embodiment;

Fig. 3 is a sectional view showing the internal  
5 structure of the image reading apparatus of the first embodiment;

Fig. 4 is a block diagram showing the arrangement of principal part of an image reading apparatus according to a second embodiment of the present  
10 invention;

Fig. 5 is a block diagram showing the arrangement of principal part of an image reading apparatus according to a third embodiment of the present invention;

Fig. 6 is a flow chart showing an outline of a  
15 normal scan operation;

Fig. 7 is a flow chart showing a timer interrupt operation according to the third embodiment;

Fig. 8 is a block diagram showing an example of  
20 the arrangement of a conventional image reading apparatus;

Fig. 9 is a flow chart showing the operation of the image reading apparatus in the first embodiment of the present invention; and

Fig. 10 is a flow chart showing the operation of  
25 the image reading apparatus in the second embodiment of the present invention.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Preferred embodiments of the present invention will be described in detail in accordance with the accompanying drawings.

### [First Embodiment]

Fig. 1 is a block diagram showing the arrangement of an image reading apparatus according to the first embodiment of the present invention, and Fig. 2 is a block diagram showing the arrangement of principal part of the image reading apparatus of the first embodiment. The principal arrangement of the image reading apparatus of the first embodiment will be described below with reference to Figs. 1 and 2.

An image reading apparatus 1 comprises a movable optical unit 4 and electric circuit board 6. In the optical unit 4, reference numeral 24 denotes a light source control circuit for turning on a light source 15. The circuit 24 comprises a so-called inverter circuit when a cold cathode tube is used as the light source 15. Reference numeral 19 denotes an imaging lens which forms an image of light emitted by the light source 15 and reflected by a document on a CCD 20 as a photoelectric conversion element.

In the electric circuit board 6, reference numerals 27R, 27G, and 27B denote analog gain adjusters, which can amplify red (R), green (G), and blue (B)

analog image signals output from the CCD 20 with  
variable gains. Reference numeral 28 denotes an A/D  
converter for converting the analog image signals  
output from the variable analog gain adjusters 27R, 27G,  
5 and 27B into digital image signals. The CCD 20, analog  
gain adjusters 27R, 27G, and 27B, and A/D converter 28  
construct an image sensor 30 shown in Fig. 2.

Reference numeral 25 denotes a motor drive  
circuit for a pulse motor 7, which outputs an  
10 excitation switching signal for the pulse motor 7 in  
response to a signal from a system controller 29.  
Reference numeral 29 denotes a system controller &  
image processor which executes image processes such as  
offset correction, shading correction, gamma correction,  
15 digital gain adjustment, resolution conversion in the  
main scan and sub-scan directions, and the like for  
digital image signals. Also, the system controller &  
image processor 29 serves as a system controller that  
stores the operation sequence of the entire image  
20 reading apparatus 1, and executes various kinds of  
control in accordance with commands from a host  
computer 21.

Reference numeral 22 denotes a communication unit  
for communicating with the host computer 21. In this  
25 embodiment, the communication unit 22 is implemented by  
USB. Alternatively, other interfaces such as IEEE1394  
and the like may be adopted.



Reference numeral 26 denotes a memory used as a working memory upon executing image processes. The memory 26 is used to store shading data for the shading process, gamma curve data for gamma correction, and the like, and is also used as a data buffer upon transferring image data to the host computer 21 via the communication unit 22. In this arrangement, the memory 26 uses a versatile RAM (random access memory).

Reference numeral 34 denotes a system bus for connecting the system controller 29 and memory 26. The system bus 34 includes address and data buses. Reference numeral 35 denotes a power supply which externally supplies electric power to the image reading apparatus 1.

Fig. 3 is a sectional view showing the internal structure of the image reading apparatus of the first embodiment.

Referring to Fig. 3, reference numeral 2 denotes a document pressing cover of the main body of the image reading apparatus 1; and 3, a document to be read. Furthermore, the image reading apparatus 1 comprises the movable optical unit 4, a platen glass 5, the electric circuit board 6, the pulse motor 7, an endless belt 8, pulleys 9 and 10, a gear train 11, a guide rail 12, and a white reference plate 13.

The optical unit 4 and pulse motor 7 are electrically connected to each other via a cable (not

shown). The optical unit 4 is slidably placed on the guide rail 12 via mount portions 14. The mount portions 14 are fixed to the endless belt 8. The optical unit 4 comprises the light source 15, a plurality of reflection mirrors 16, 17, and 18, the imaging lens 19, and the CCD 20.

A normal document image reading operation of the image reading apparatus 1 with the above arrangement will be briefly described below.

10 The image reading apparatus 1 starts a reading operation in response to a read command from the host computer 21. The image reading apparatus 1 turns on the light source 15 in the optical unit 4, makes the plurality of mirrors 16, 17, and 18 reflect light reflected by a document, and forms an image of the reflected light on the CCD 20 via the imaging lens 19, thus reading an image for one line in the main scan direction.

The drive force of the pulse motor 7 is transmitted to the pulley 9 via the gear train 11 to rotate the pulley 9, thereby driving the endless belt 8. In this way, the optical unit 4 fixed to the endless belt 8 via the mount portions 14 moves along the guide rail in the sub-scan direction indicated by arrow X.

25 The image reading apparatus 1 repeats to read a line image in the main scan direction while moving the optical unit 4 in the sub-scan direction.

The image reading apparatus 1 moves the optical unit 4 in the sub-scan direction indicated by arrow X up to a position indicated by a dotted line 4 in Fig. 3, thus scanning the entire surface of the platen glass 5.

5        Note that a partial image of a document on the platen glass 5 can also be read in accordance with the contents of a read command from the host computer 21. In such case, the system controller & image processor 29 specifies the range of pixels to be adopted of the  
10        outputs from the image sensor 30 in the main scan direction, and the moving range of the optical unit 4 in the sub-scan direction, so as to attain the range of an image to be read designated by the host computer 21.

      In the above arrangement, the first embodiment  
15        further comprises a reset circuit 50 for resetting the system controller 29 when the power supply 35 is turned on, and a power supply line detection circuit 51 as a characteristic feature of the first embodiment. Note that the power supply line detection circuit 51 detects  
20        a change in potential of a power supply line included in the USB cable 22. When a power supply voltage to be supplied from the host computer 21 stops upon unplugging the USB cable 21 from the image reading apparatus 1 and/or the host computer 21, the detection  
25        circuit 51 outputs a reset signal (Reset) to the system controller 29.

Upon receiving the reset signal, the system controller 29 is reset since it is set in the same state as that upon receiving a reset signal (Reset) from the reset circuit 50 upon power ON of the power supply 35. The system controller 29 controls the light source 15 via the light source control circuit 24, and the pulse motor 7 via the motor drive circuit 25. In this case, since the system controller 29 is reset, their control states are also reset. In the reset state, the light source 15 is turned off, and a current to the pulse motor 7 is stopped.

The operation of the first embodiment will be described below with reference to Fig. 9.

When the power supply 35 of the image reading apparatus 1 is turned on, the system controller 29 is reset in response to the reset signal output from the reset circuit 50 (step S11). The power supply line detection circuit 51 checks if a power supply potential V1 of the power supply line included in the USB cable 22 is lower than a predetermined level L1 (step S12). If YES in step S12, step S12 is repeated to monitor the power supply potential V1 while maintaining the reset state. If the power supply potential V1 is equal to or higher than the predetermined level L1, it is determined that the USB cable 22 used to communicate with the host computer 21 is normally connected, and

operation is executed in a normal image reading mode (normal mode) (step S13).

The power supply line detection circuit 51 continuously monitors the power supply potential V1 of the power supply line included in the USB cable 22 (step S14). If the circuit 51 detects that the power supply potential V1 becomes lower than the predetermined level L1 (YES in step S14), it determines that a communication with the host computer 21 is abnormal or is disconnected, the circuit 51 issues a reset signal (Reset) to the system controller 29 (step S15).

Upon receiving the reset signal, the system controller 29 is reset (step S16). Since the system controller 29 is reset, control of the light source 15 via the light source control circuit 24, and that of the pulse motor 7 via the motor drive circuit 25 by the system controller 29 are also reset. In this reset state, since the light source 15 is turned off, and a current to be supplied to the pulse motor 7 is stopped, a power saving mode is set. That is, in the first embodiment, currents consumed by the light source 15 and pulse motor 7 assume minimum values.

Since the system controller 29 is reset, supply of clock signals used to operate the image sensor 30 incorporated in the apparatus, and access to the memory 26 also stop.

As described above, according to the first embodiment, in an image reading apparatus with a simple arrangement which incorporates neither a CPU nor a large-scale hardware timer, when the USB cable 22 as  
5 the communication unit with the host computer 21 is unplugged during operation in the normal mode, and the power supply line included in the USB cable 22 is disconnected, the system controller 29 is reset before the apparatus operation becomes unstable, and currents  
10 consumed by the light source 15 and pulse motor 7 instantaneously become minimum values. In addition, when the system controller 29 is reset, since supply of clock signals and access to the memory 26 stop, consumption power in the apparatus can be minimized.

15 In the above description, the light source 15 is turned off. Alternatively, to assure a shorter rise time after the communication has restarted, the energization amount may be lowered.

[Second Embodiment]

20 Fig. 4 is a block diagram showing the arrangement of principal part of an image reading apparatus according to the second embodiment of the present invention. The same reference numerals in Fig. 4 denote the same parts as in Fig. 2, and a description  
25 thereof will be omitted.

The arrangement of the second embodiment comprises a data line detection circuit 61 in place of

the power supply line detection circuit 51 in the arrangement of the first embodiment shown in Figs. 1 and 2.

5 The data line detection circuit 61 detects a change in potential on a data line in the USB cable 22, and outputs a sleep signal (Sleep) to the light source control circuit 24 and pulse motor drive circuit 25 when the data line is disconnected upon unplugging the USB cable 22 or any abnormality is found.

10 The light source control circuit 24 and pulse motor drive circuit 25 respectively control the light source 15 and pulse motor 7 under the control of the system controller 29. The sleep signal issued by the data line detection circuit 61 has higher priority than  
15 control signals normally received from the system controller 29. In a sleep state in which the sleep signal is issued, the light source 15 is turned off, and currents to be supplied to the pulse motor 7 are stopped.

20 The operation of the second embodiment will be described below with reference to Fig. 10.

When the power supply 35 of the image reading apparatus 1 is turned on, the operation starts in the normal image reading mode (normal mode) in step S21.  
25 The data line detection circuit 61 checks if a potential V2 of the data line included in the USB cable 22 is lower than a predetermined level L2 (step S22).

If the potential V2 of the data line is equal to or higher than the predetermined level L2, since it is determined that the USB cable 22 used to communicate with the host computer 21 is normally connected, the  
5 flow returns to step S22 to proceed with operation in the normal mode without issuing a sleep signal, thus monitoring the power supply and the potential V2 on the data line.

On the other hand, if the data line detection  
10 circuit 61 detects that the potential V2 of the data line included in the USB cable 22 becomes lower than the predetermined level L2 (YES in step S22), the data line detection circuit 61 determines that a communication with the host computer 21 is disconnected,  
15 and issues a sleep signal (Sleep) to the light source control circuit 24 and pulse motor drive circuit 25 (step S23).

Upon receiving the sleep signal, the light source control circuit 24 and pulse motor drive circuit 25 set  
20 their operations in a sleep state since the sleep signal has priority over normal control signals received from the system controller 29. In this sleep state, predetermined circuits are set in a power saving mode. That is, in the second embodiment, the light  
25 source 15 is turned off, and currents to be supplied to the pulse motor 7 are stopped.



As described above, in the second embodiment, in an image reading apparatus with a simple arrangement which incorporates neither a CPU nor a large-scale hardware timer, when the USB cable 22 as the  
5 communication means with the host computer 21 is unplugged from the image reading apparatus 1 and/or the host computer 21, and the data line included in the USB cable 22 is disconnected, since the light source control circuit 24 and pulse motor drive circuit 25 are  
10 set in a sleep state before the operation of the apparatus becomes unstable, currents consumed by the light source 15 and pulse motor 7 instantaneously become minimum values. That is, electric power supplied to the light source 15 and pulse motor 7 that  
15 may consume the largest electric power in the apparatus is minimized, thus nearly minimizing consumption power in the apparatus.

In the above description, the light source 15 is turned off. Alternatively, to assure a shorter rise  
20 time after the communication has restarted, the energization amount may be lowered.

[Third Embodiment]

In the first and second embodiments, the image reading apparatus does not comprise any CPU.  
25 Alternatively, the present invention may be applied to an apparatus that comprises a CPU. In the third

embodiment, an image reading apparatus that comprises a CPU in a system controller will be explained below.

Fig. 5 is a block diagram showing the arrangement of principal part of an image reading apparatus according to the third embodiment of the present invention. The same reference numerals in Fig. 5 denote the same parts as in Fig. 2, and a description thereof will be omitted.

The arrangement of the third embodiment comprises a CPU and the like in a system controller in the arrangement of the first embodiment shown in Figs. 1 and 2. That is, a system controller 29A of the third embodiment comprises an image processing circuit 29-1, CPU 29-2, ROM 29-3, and RAM 29-4. Furthermore, the power supply line detection circuit 51 detects whether or not a power supply voltage of the USB cable 22 is normally supplied from the host computer 21, and outputs an ON detection signal VbusON to the system controller 29A if the power supply voltage is normally supplied. Note that the detection signal VbusON is output to the system controller 29A via a route different from that of a reset signal output from the reset circuit 50.

Fig. 6 is a flow chart showing an outline of the normal scan operation in the third embodiment, and Fig.7 is a flow chart showing the timer interrupt

operation as a characteristic feature of the third embodiment.

Referring to Fig. 6, if the power supply 35 of the apparatus is turned on (step S201), the CPU 29-2 is  
5 reset in response to an output (Reset signal) from the reset circuit 50. Initial values are set in various flags and memory in the system controller 29A (step S202), and the control waits for an instruction from the host computer 21 (step S203). If the host computer  
10 21 confirms connection with the image reading apparatus 1, it issues a reset instruction to the image reading apparatus 1. The image reading apparatus 1 adjusts the light source 15 to turn it on, and executes initialization such as setup of the home position of  
15 the pulse motor 7, acquisition of shading correction data, permission of timer interrupt, and the like in accordance the initialization instruction (step S204).

After that, the control waits for an image read scan instruction from the host computer 21 (step S205).  
20 Upon receiving the image read scan instruction, clock signals CLK are supplied to the image sensor 30, and the pulse motor 7 is driven to scan a document image, thereby reading an image within the read range designated by the host computer 21. The read image  
25 data is then output to the host computer 21 (step S206).

The timer interrupt operation as a characteristic feature of the third embodiment will be described below

with reference to Fig. 7. Assume that an interrupt is set to be generated at 1-msec intervals in initialization. When a program according to the flow chart in Fig. 7 is stored in the ROM 29-3 in the system controller 29A to operate the apparatus, the following control method can be implemented.

Upon generation of a timer interrupt (step S210), it is checked if the input VbusON from the power supply line detection circuit 51 is ON (step S211). Since the power supply voltage of USB cannot be detected when the USB cable 22 is not connected to the host computer 21 or when any abnormality is detected upon inadvertently unplugging a connector to disrupt a communication in progress, the signal VbusON is OFF.

If the signal VbusON is OFF, the CPU is set in a sleep state even during an image reading process, and supply of clock signals CLK to the image sensor 30 is stopped. Also, an emission stop signal is supplied to the light source control circuit 24 to turn off the light source 15, and a stop signal is output to the pulse motor drive circuit 25 to stop the pulse motor 7 even during driving (step S212).

In the third embodiment, the power saving mode is set for some respective circuits, e.g., a clock IC, the light source lamp, motor, and the like, but some circuits need not always be completely turned off and their consumption power need only be reduced in the

power saving mode (the same applies to other embodiments).

If it is detected that the signal VbusON is ON, the interrupt process normally ends (step S213).

5 As described above, according to the third embodiment, in an image reading apparatus which comprises a CPU, since disconnection of a communication can be detected without monitoring the communication by the CPU, the program size can be reduced, and power  
10 savings in the image reading apparatus can be achieved.

If it is detected that the signal VbusON is OFF, the pulse motor 7 is controlled to be merely immediately stopped. Alternatively, in the third embodiment that comprises the CPU 29-2, the pulse motor  
15 7 may be stopped after it is controlled to be returned to its home position, i.e., to reset its mechanical position.

The present invention is not limited to the apparatuses of the aforementioned embodiments, and  
20 includes not only an apparatus which executes initialization immediately after any abnormality of a communication path is detected, but also an apparatus which executes initialization after it normally recovers the communication path.

25 Further, the object of the present invention can also be achieved by providing a storage medium storing program codes for performing the aforesaid processes to

a computer system or apparatus (e.g., a personal computer), reading the program codes, by a CPU or MPU of the computer system or apparatus, from the storage medium, then executing the program.

5           In this case, the program codes read from the storage medium realize the functions according to the third embodiment, and the storage medium storing the program codes constitutes the invention.

10           Further, the storage medium, such as a floppy disk, a hard disk, an optical disk, a magneto-optical disk, CD-ROM, CD-R, a magnetic tape, a non-volatile type memory card, and ROM can be used for providing the program codes.

15           Furthermore, besides aforesaid functions according to the above third embodiment are realized by executing the program codes which are read by a computer, the present invention includes a case where an OS (operating system) or the like working on the computer performs a part or entire processes in  
20           accordance with designations of the program codes and realizes functions according to the above third embodiment.

25           Furthermore, the present invention also includes a case where, after the program codes read from the storage medium are written in a function expansion card which is inserted into the computer or in a memory provided in a function expansion unit which is

connected to the computer, CPU or the like contained in  
the function expansion card or unit performs a part or  
entire process in accordance with designations of the  
program codes and realizes functions of the above  
5   embodiments.

          In a case where the present invention is applied  
to the aforesaid storage medium, the storage medium  
stores program codes corresponding to the flowchart in  
Fig. 7 described in the third embodiment.

10       The present invention is not limited to the above  
embodiments and various changes and modifications can  
be made within the spirit and scope of the present  
invention. Therefore to apprise the public of the  
scope of the present invention, the following claims  
15   are made.